

AnaGen

Disruptive methods for the next generation of Chip Design







SummerSchool 2023

Wouldn't it be great if we could automatically create analog designs for our chips – from electrical specifications to a final layout? Fully automatically generated circuits have been the holy grail of analog IC design for more than 30 years. We believe now is the time to master this challenging task with groundbreaking, disruptive methods.

When: 18-22 September 2023

Where: Infineon Technologies Austria

Who: Technical students (BSc, MSc, PhD) – especially

electrical engineering and computer science students

Spread the word!

Application Deadline: 31, August 2023

Apply now! For more information, scan the QR code or go to **www.infineon.com/schools**

